

De : [Gilles de France](#)  
A : [Turzo Ketel](#)  
Objet : Fwd: Re: Visit\_in\_Cracow  
Date : mardi 15 juillet 2014 16:17:36

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Le 20/04/2011 13:50, Adam Czermak a écrit :

Dear Michel,

All three of you have a reservation for 3 rooms for 3 nights (9.05-12.05) in the centre, unfortunately it is with breakfast...The price is 185 zł./night/person. Each of you will receive 900 zł. as allowances from IFJ PAN for your visit in Cracow.

The address of the Hotel place is;

B&B Unikat  
ul. Wiślna 10

31-007 Kraków  
[www. noclegi-krakow.pl](http://www.noclegi-krakow.pl)

tel. 12 4219871

<http://accommodation.com.pl/index.html>

Concerning our meeting, be sure we try to make our best, but still we are not able to check

communication with SFP transceivers without the GTS card (of course ET's example works).

The major pieces of GTS\_LEAF cores exists but not tested. Unfortunately, we in Cracow

don't have any software support, so we hope to make main progress with your participation.

I do not think that any exchange of block schematics, IPs, programs can be helpful for any of

us before the meeting in Cracow.

All the best for Easter and the spring season.

Adam

**On Fri, 15 Apr 2011 16:07:25 0200, tripon wrote**

> Hi Adam and Barbara

>

> I confirm our visit to your lab. Abder Boujrad, Luigi Martina and myself will take part to the 2,5 days meeting, starting from Tuesday 10th May to Thursday noon 12th May. Radek from Warsaw will join us on Wednesday.

> Here are our flight hours:

> - Arrival: Monday 9th May at 18h15, Krakow

> - Departure: Thursday 12th May at 15h10

>

> May I ask you to book us accommodations for 3 nights, breakfast not included. We prefer to have it elsewhere

- > On Monday, we will take a taxi from the airport to downtown; don't forget to send me the accommodation address.
- > On Tuesday, we can take a taxi to your lab or you can drive us to your lab; it's up to you.
- >
- > Here are some suggestions and comments
- > For Adam:
  - > About the agenda, you are right we have many items to discuss. But if everyone takes time to prepare his task, we will save a lot of time. So, I insist you make your best to have your ML507 test bench working properly with the IPs downloaded into the Virtex5.
  - > Did you have time to have a look at ET's Project? We may have time to connect GTS\_V3 and GTS\_Virtex5\_like. I will bring one GTS\_V3 mezzanine. Next week I will send you the GTS tree setup software. I hope you have some software support in your lab.
  - > Do you have optical fibers and transceivers?
  - > Before meeting us, I would like you send me your XPS project and the GTS IP block diagram showing IPs connections.
  - > For Barbara
    - > Have you modified the GTS hardware drawing in order to include the multiple clocks, the delay lines and PLLs?
    - > Have you define signals from/to peripherals, firmware nets to the ADC interface taken in charge by Radek?
    - > Before we meet, I would like you send me a block diagram of the clock management and the updated list of I/O signals and firmware nets.
  - >
  - > Don't hesitate to ask me more info.
  - > I am looking forward to getting your documents.
  - > Have a nice week end
  - > Michel
  - >
  - > Le 31/03/2011 13:38, Adam Czermak a écrit :

- > Dear Michel,
- > Thank you for the agenda of the visit in Cracow in May. Your suggested dates (10-11th May) for the visit, it is OK for us.
- > Only one suggestion - if you can stay with us one day longer (let say 12-th of May) it can be profitable for both sides,
- > as your agenda is rather long and some of the tasks are time consuming and perhaps must be repeated few times.
- > The GTS Mezzanine will help a lot, but also if you come with your ML507 board and Xilinx DOWNLOAD cable,
- > then we will be able to progress on two setups in parallel and speed up the tests..
- > We can provide the Xilinx licence files for yours and Luigi's laptops at the IFJ.
- > All the best and to see you in May,
- >

Adam and Barbara

- >
- > **On Wed, 30 Mar 2011 10:44:38 0200, tripon wrote**
- > > Dear Barbara and Adam
- > >
- > > I would like to fix dates and agenda of our visit to your lab
- > > Luigi will bring a ML507 base project running under Linux and he will need a connection to your Xilinx licence server
- > >
- > > - Visit dates:
- > > We suggest to have two full days meeting: Tuesday 10th and Wednesday 11th of May
- > > We plan to arrive lately on Monday 9th and leave early on Thursday 12th.
- > > - Agenda:

> > a) Analysis of your GTS leaf project on ML507  
 > > b) Test of the GTS registers access: Xilkernel (what you are doing) and Linux (Luigi's base project)  
 > > c) GTS IP analysis: block diagram and firmware, IP interface to PPC.  
 > > d) Implementation of ET's ISE project in your project  
 > > e) GTS leaf schematics (block diagram, FPGA Inputs and outputs, peripheral components, clock management)  
 > > f) ET's GTS light tree software and its implementation in your lab (I will bring a GTS V3 mezzanine)  
 > > g) GTS IP connection to ADC interface IP.  
 > > h) GTS task planning  
 > > - Needs  
 > > Could you tell me about Luigi's laptop connection to your Xilinx licence server?  
 > > - Meeting with Radek Tarnowski  
 > > I would like to take the opportunity of my visit in Poland for meeting Radek Tarnowski. We could have a discussion about the IP ADC interface he has in charge and see that IP connection to the GTS IP.  
 > > Tell me what is your feeling?  
 > >  
 > > Please reply to my email as soon as possible, so that we could book plane ticket  
 > > Best regards  
 > > Looking forward to seeing you both  
 > > Michel  
 > >  
 > > Le 21/03/2011 15:03, Adam Czermak a écrit :

Dear Gilles, Dear

Michel,

The work on the GTS leaf is progressing. Me and Barbara

are almost 90% involved in the design, but progress takes

more time than expected. We are preparing one component

"GTS\_IP" containing all of functional GTS IPs in ISE environment

and the plb\_cracus IP in EDK, as Michel would like to

have.  
We think this will be the best way for implementation  
of

GTS  
function in NUMEXO\_2 card. The first merging of the

GTS\_IP  
and plb\_cracus to the ppc440 in EDK will start very

soon.  
In two - three weeks we should be ready to combine  
these

designs  
in one module and start to debug on the ML507 card. It  
would be

nice  
if you and the designer from your side will spend a  
week with

us  
in Cracow during this procedure. We have holidays from  
1.05 -

3.05.

So if you plan to join this effort the week before or  
after

Easter  
or second week of May it seems to be the best time to  
come to

Cracow.

Heaving the GTS\_V3 in our hands will speed up planed

tests.  
Please let us know your

plans.

With the best

regards,

Adam &

Barbara

On Thu, 17 Mar 2011 13:41:45 0100, Gilles de France

wrote

Dear Barbara and

Adam,

I have asked Michel to make, for me, a progress report on the

various tasks within the EXOGAM2

project.

Concerning the GTS task itself, I had a look to the planning

(attached) and the accomplishments which should have been done so

far. It seems to me that we are in a critical situation for this

task and I would like to remedy this situation which potentially can

be a major problem for the whole EXOGAM2 development, as soon as

possible.

Indeed I do not see significant progresses in the various subtasks

for which you -in Krakow- have the responsibility on and this is

worrying me. The question is then: how to

improve?

Is this a manpower issue? If I remember correctly, you estimated

your involvement in EXOGAM2 to 80% for Barbara and 20% for Adam. I

know that Adam has been away for some time  
(I hope you are going

better now by the way) and this might be  
the origin of the slow

progresses. I can also easily believe that  
you are loaded by many

other duties which then makes of EXOGAM2 a  
"side project". But we

definitively and desperately need your full

involvement.

Is there any specific technical difficulty?  
Or misunderstanding on

what you should deliver? You agreed some  
time ago on the attached

planning and task distribution: can you  
confirm that it is still



valid?

It seems to me that the link between  
Krakow and GANIL should be

revitalized

and  
strengthen by any possible kind. Thus I  
suggest that you and Michel

organize

regular  
phone meetings (every 15 days for instance)  
so that there is a real

and constant estimate on

progresses/difficulties/etc.

Finally, I also suggest that Michel and I  
come to Krakow within a

month so that you can report the

progresses.

Would you agree on these

suggestions?

As I said, we really need you help and  
full commitment otherwise we

are in big

troubles.

With my best

regards,  
Gilles

>  
>